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Micron Docket No.: 98-1080
Docket No.: M4065.0422/P422
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Howard E. Rhodes

Application No.: 09/941,546

Group Art Unit: 2811

Filed: August 30, 2001

Examiner: Not Yet Assigned

For: CMOS IMAGER AND METHOD OF
FORMATION

**POWER OF ATTORNEY BY ASSIGNEE AND
CERTIFICATE BY ASSIGNEE UNDER 37 CFR § 3.73(B)**

Assistant Commissioner for Patents
Washington, DC 20231

Dear Sir:

Micron Technology, Inc., Assignee of the entire right, title and interest in the above-identified application by virtue of the Assignment attached hereto (which is also being submitted concurrently for recordation), hereby appoints the attorneys and agents of the firm of Dickstein Shapiro Morin & Oshinsky LLP located at 2101 L Street NW, Washington, DC 20037-1526, listed as follows:

Gary M. Hoffman	26,411	Laurence E. Fisher	37,131	Gianni Minutoli	41,198
Thomas J. D'Amico	28,371	John C. Luce	34,378	Michael Bergman	42,318
Donald A. Gregory	28,954	William E. Powell, III	39,803	Mark E. Strickland	45,138
James W. Brady, Jr.	32,115	Gary L. Veron	39,057	Salvatore P. Tamburo	45,153
Jon D. Grossman	32,699	Mialecka C. Williams-	48,037	Peter Veytsman	45,920
Mark J. Thronson	33,082	Jeremy A. Cubert	40,399	Christopher S. Chow	46,493
Eric Oliver	35,307				

and also, listed as follows:

Michael Lynch	30,871	David Paul	34,692	Charles Brantley	38,086
Kevin Martin	37,882	Walter Fields	37,130		

attorneys of Micron Technology, Inc. as its attorneys with full power of substitution to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith.

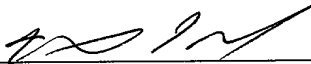
The Assignee certifies that the above-identified assignment has been reviewed and to the best of the Assignee's knowledge and belief, title is in the assignee.

Please direct all correspondence regarding this application to the following:

Thomas J. D'Amico
DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP
2101 L Street NW
Washington, DC 20037-1526
Telephone: (202) 785-9700
Fax: (202) 887-0689

Respectfully submitted,

MICRON TECHNOLOGY, INC.



Michael L. Lynch
Chief Patent Counsel
Registration No. 30,871

Dated: 9/7/01

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